Docket No.: 60188-108

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Kazuhiro OKABAYASHI, et al.

Serial No.: 09/973,888

Filed: October 11, 2001

Customer Number: 20277

Confirmation Number: 8960

Group Art Unit: 2116

Examiner: Paul B. Yanchus, III

For: INTEGRATED CIRCUIT WITH MULTIPROCESSOR ARCHITECTURE

Mail Stop Amendment Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

No additional fee is required.

Applicant is entitled to small entity status under 37 CFR 1.27

Also attached:

The fee has been calculated as shown below:

| | NO. OF CLAIMS | HIGHEST PREVIOUSLY PAID FOR | EXTRA CLAIMS | RATE | FEE |
|-----------------------------|------------------|-----------------------------------|-----------------|-----------|--------|
| Total Claims | 7 | 20 | 0 | \$18.00 = | \$0.00 |
| Independent Claims | 3 | 3 | 0 | \$86.00 = | \$0.00 |
| | | Multiple claims newly presented | | | \$0.00 |
| | | Fee for extension of time | | | \$0.00 |
| | | | | | \$0.00 |
| Total of Above Calculations | | | | \$0.00 | |

Please charge my Deposit Account No. <u>500417</u> in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

MCDERMOTT WILL & EMERY LLP

Registration No. 36, 1'39

Michael E. Fogart

600 13th Street, N.W. Washington, DC 20005-3096 202.756.8000 MEF:mcm Facsimile: 202.756.8087 **Date: September 10, 2004**



2116 Shv

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Kazuhiro OKABAYASHI, et al. : Confirmation Number: 8960

Serial No.: 09/973,888 : Group Art Unit: 2116

Filed: October 11, 2001 : Examiner: Paul B. Yanchus, III

For: INTEGRATED CIRCUIT WITH MULTIPROCESSOR ARCHITECTURE

AMENDMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 10, 2004, having a three-month shortened statutory period for response set to expire on September 10, 2004, reconsideration of the above-identified application is respectfully requested in view of the following amendment and remarks.